



Product/Process Change Notice - PCN 17_0023 Rev. -

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title: AD9694 Data Sheet Specification Change

Publication Date: 13-Dec-2017

Effectivity Date: 13-Dec-2017 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Initial Release

Description Of Change

AD9694 Product Data Sheet is being updated from Rev 0 to Rev A.

Summary of changes to the data sheet specifications are as follows:

- 1) Table 6' minimum Unit Interval (UI) changed from 62.5 to 66.67 ps.
- 2) Table 6' minimum Data Rate per Channel changed from 1.5625 to 1.6875 Gbps.
- 3) Input Clock Detect section on page 27' Removed bit 2 in register 0x011A. Disabling clock detect is no longer supported.
- 4) Edited Clock Duty Cycle Considerations section on page 27' Enabling or disabling the Duty Cycle Stabilizer (DCS) now requires writes to registers 0x011C, 0x011E, and 0x011F.

Reason For Change

The data sheet is being updated to accurately reflect the AD9694 device capabilities.

Impact of the change (positive or negative) on fit, form, function & reliability

The changes described above will not have any impact on fit, form, or reliability of the device.

Summary of Supporting Information

Changes will be reflected in rev A of the Product Data Sheet.

Supporting Documents None

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas: PCN_Americas@analog.com

Europe: PCN_Europe@analog.com

Japan: PCN_Japan@analog.com

Rest of Asia: PCN_ROA@analog.com

Appendix A - Affected ADI Models

Added Parts On This Revision - Product Family / Model Number (2)

AD9694 / AD9694BCPZ-500

AD9694 / AD9694BCPZRL7-500

Appendix B - Revision History

Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	13-Dec-2017	13-Dec-2017	Initial Release

Analog Devices, Inc.

DocId:4013 Parent DocId:None Layout Rev:7